

<b>Notice of References Cited</b>	Application/Control No. 09/741,846	Applicant(s)/Patent Under Reexamination MODELSKI ET AL.	
	Examiner XUAN M. THAI	Art Unit 2181	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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7	B	US-5,805,837	09-1998	Hoover et al.	710/110
7	C	US-5,996,036	11-1999	Kelly, James D.	710/110
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	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
7	U	"Tagged Inter Processor Communication Bus for Multiprocessor Systems" IBM Technical Disclosure Bulletin, August 1991, vol. 34, issue 3, pages 115-121.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

